

# LIQUID CRYSTAL DISPLAY MODULE

# **Product Specification**

| PRODUCT<br>NUMBER | LWD70620EW640G320WID |
|-------------------|----------------------|
|                   |                      |

| INTERNAL    | APPROVALS        |
|-------------|------------------|
| Engineering | Document Control |
| Michael Gu  |                  |

| Product No. | LWD70620EW640G320WID | REV. A |  |
|-------------|----------------------|--------|--|
|-------------|----------------------|--------|--|



# **TABLE OF CONTENTS**

| 1  | $\mathbf{M}$             | AIN FEATURES   | 4        |
|----|--------------------------|--|----------|
| 2  | M                        | ECHANICAL DRAWING  | 5        |
| 3  | AI                       | SSOLUTE MAXIMUM RATINGS  | 6        |
| 4  | EI                       | ECTRICAL CHARACTERISTICS   | 7        |
|    | 4.1<br>4.2               | OPERATING CONDITIONSLED DRIVING CONDITIONS   | 7        |
| 5  | DO                       | C CHARACTERISTICS  | 8        |
| 6  | <b>A</b> (               | C CHARACTERISTICS  | 9        |
|    | 6.1<br>6.2<br>6.3<br>6.4 | SYSTEM BUS TIMING FOR 6800 SERIES MPUSYSTEM BUS TIMING FOR 8080 SERIES MPUSYSTEM BUS TIMING FOR 4-LINE SERIAL INTERFACESYSTEM BUS TIMING FOR 3-LINE SERIAL INTERFACE | 10<br>11 |
| 7  | OI                       | PTICAL CHARACTERISTICS   | 13       |
| 8  | IN                       | TERFACE PIN ASSIGNMENT   | 15       |
|    | 8.1                      | BACKLIGHT UNIT SECTION   | 16       |
| 9  | BI                       | OCK DIAGRAM  | 17       |
| 10 | RF                       | ELIABILITY   | 18       |
|    | 10.1                     | RELIABILITY TEST   | 18       |
| 11 | IN                       | ITIAL CODE   | 19       |
| 12 | PA                       | ART NUMBER LABELLING   | 21       |
| 13 | B PA                     | ART NUMBER DECODER   | 22       |
| 14 | l H                      | ANDLING PRECAUTIONS  | 23       |

| Product No. | LWD70620EW640G320WID | REV. A | Page | 2 / 23 |
|-------------|----------------------|--------|------|--------|



#### **REVISION RECORD**

| Rev. | Date     | Page | Sec. | Comment               | ECN<br>No. |
|------|----------|------|------|-----------------------|------------|
| A    | 03/11/16 |      |      | New Densitron Release | E5195      |
|      |          |      |      |                       |            |
|      |          |      |      |                       |            |
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| Product No. | LWD70620EW640G320WID | RFV A |
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| Page | 3 / 23 |
|------|--------|



# 1 MAIN FEATURES

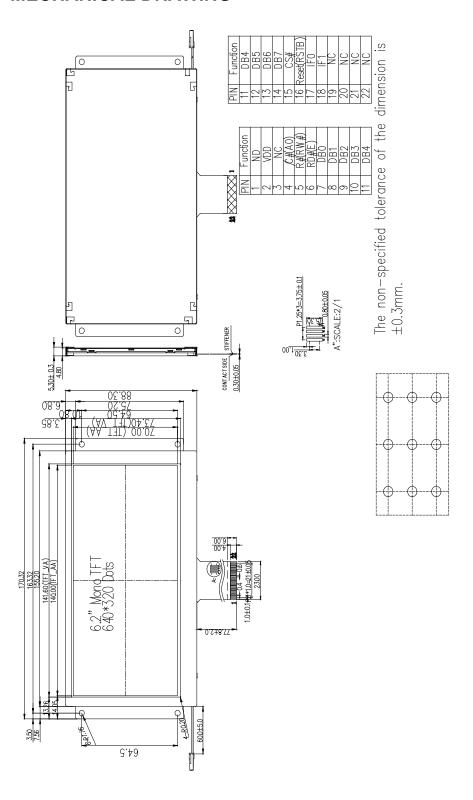
| ITEM                  | DESCRIPTION                         | UNIT |
|-----------------------|-------------------------------------|------|
| Display Format        | 640 x 320                           | dots |
| Outline Dimension     | 170.32 (W) x 88.3 (H) x 5.3 (D)     | mm   |
| Viewing Area          | 141.60 (W) x 73.40 (H)              | mm   |
| Active Area           | 140.00 (W) x 70.00 (H)              | mm   |
| LCD Type              | TFT / Transmissive / Normally Black |      |
| Viewing Angle         | 80/80/80                            |      |
| Backlight             | LED                                 |      |
| Color                 | White                               |      |
| Interface             | Parallel/Serial                     |      |
| Operating Temperature | -20 ~ 70                            | °C   |
| Storage Temperature   | -30 ~ 80                            | °C   |
| RoHS Compliant        | Yes                                 |      |

| Product No.  | LWD70620EW640G320WID | REV A    |  |
|--------------|----------------------|----------|--|
| r roduct no. |                      | IND V. A |  |

| Page | 4 / 23 |
|------|--------|

# 2 MECHANICAL DRAWING





| Product No. | LWD70620EW640G320WID | REV. A |
|-------------|----------------------|--------|

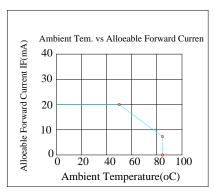
| Page | 5 / 23 |
|------|--------|



# 3 ABSOLUTE MAXIMUM RATINGS

| Item                  | Symbol | Min | Тур | Max | Unit |
|-----------------------|--------|-----|-----|-----|------|
| Operating Temperature | TOP    | -20 | -   | +70 | °C   |
| Storage Temperature   | TST    | -30 | -   | +80 | °C   |

- 1. Device is subject to be damaged permanently if it is stressed beyond the above absolute maximum ratings listed above.
- 2. Temp. ≤60°C, 90% RH MAX. Temp. > 60°C, Absolute humidity shall be less than 90% RH at 60°C



| Product No. LWD70620EW640G320WID REV. A |  |
|---|--|
|---|--|

| Page | 6 / 23 |
|------|--------|



# 4 ELECTRICAL CHARACTERISTICS

#### 4.1 OPERATING CONDITIONS

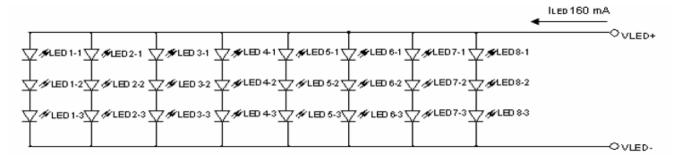
| Item                   | Symbol | Condition | Min | Тур | Max | Unit | Remark |
|------------------------|--------|-----------|-----|-----|-----|------|--------|
| Supply Voltage For LCM | VDD    | -         | 3.0 | 3.3 | 3.6 | V    |        |
| Supply Current For LCM | IDD    | -         | -   | 25  | 38  | mA   | Note1  |
| Power Consumption      | -      | -         | -   | 83  | 137 | mW   |        |

Note:

1. This value test is for VDD = 3.3V only.

#### 4.2 LED DRIVING CONDITIONS

| Parameter         | Symbol | Min. | Typ.   | Max. | Unit | Remark     |
|-------------------|--------|------|--------|------|------|------------|
| LED current       |        | -    | 160    | -    | mA   |            |
| Power Consumption |        | 1392 | -      | 1680 | mW   |            |
| LED voltage       | VLED+  | 8.7  | 9.6    | 10.5 | V    | Note 1     |
| LED Life Time     |        | -    | 20,000 | -    | Hr   | Note 2,3,4 |



- 1. Power supplied to the backlight specification.
- 2.  $Ta = 25^{\circ}C$
- 3. Brightness to be decreased to 50% of the initial value.
- 4. Single LED lamp case.

|            |    |                      |        | • |      |        |
|------------|----|----------------------|--------|---|------|--------|
| Product No | 0. | LWD70620EW640G320WID | REV. A |   | Page | 7 / 23 |



# 5 DC CHARACTERISTICS

| Parameter                | Symbol          |        | Rating | Unit   | Condition |           |
|--------------------------|-----------------|--------|--------|--------|-----------|-----------|
| Tarameter                | Symbol          | Min    | Тур    | Max    | Cint      | Condition |
| Low level input voltage  | $V_{\text{IL}}$ | 0      | -      | 0.3VDD | V         |           |
| High level input voltage | V <sub>IH</sub> | 0.7VDD | -      | VDD    | V         |           |

| Product No. LWD/0620EW640G320WID REV. A | Product No. | LWD70620EW640G320WID | REV. A |
|---|-------------|----------------------|--------|
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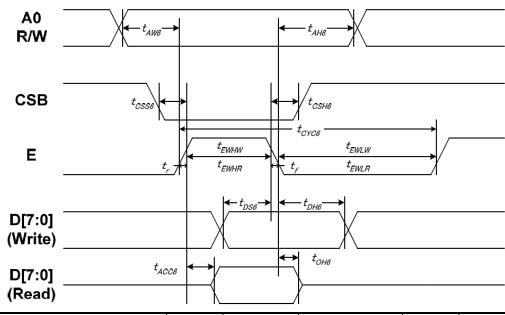
| Page | 8 / 23 |
|------|--------|



#### 6 AC CHARACTERISTICS

#### 6.1 SYSTEM BUS TIMING FOR 6800 SERIES MPU

System Bus Timing for 6800 Series MPU



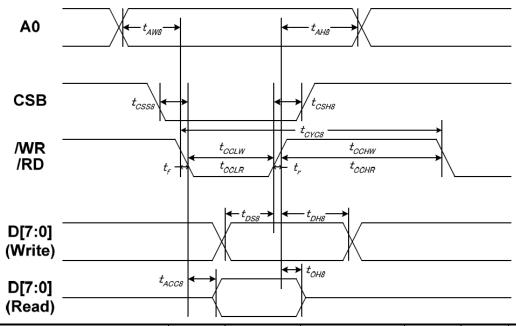
| Item                          | Signal | Symbol | Condition    | Min | Max | Unit |
|-------------------------------|--------|--------|--------------|-----|-----|------|
| Address setup time            | A0     | tAW6   | -            | 10  | -   |      |
| Address hold time             | AU     | tAH6   | 1            | 0   | -   |      |
| System cycle time             |        | tCYC6  | -            | 200 | -   |      |
| Enable L pulse width (WRITE)  |        | tEWLW  | -            | 100 | -   |      |
| Enable H pulse width (WRITE)  | Е      | tEWHW  | •            | 100 | -   |      |
| Enable L pulse width (READ)   |        | tEWLR  | 1            | 130 | -   |      |
| Enable H pulse width (READ)   |        | tEWHR  | 1            | 130 | -   | ns   |
| CSB setup time                | CSB    | tCSS6  | 1            | 100 | -   |      |
| CSB hold time                 | CSB    | tCSH6  | 1            | 100 | -   |      |
| Write data setup time         |        | tDS6   | •            | 70  | -   |      |
| Write data hold time          | D[7.0] | tDH6   | -            | 20  | -   |      |
| Read data access time         | D[7:0] | tACC6  | CL = 100  pF | ı   | 80  |      |
| Read data output disable time |        | tOH6   | CL = 100 pF  | 15  | 80  |      |

- 1. The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. When the system cycle time is extremely fast,  $(tr + tf) \le (tCYC8 tCCLW tCCHW)$  for  $(tr + tf) \le (tCYC8 tCCLR tCCHR)$  are specified.
- 2. All timing is specified using 20% and 80% of VDDI as the reference.
- 3. tCCLW and tCCLR are specified as the overlap between CSB being "L" and /WR and /RD being at the "L" level.CSB and /WR (or /RD) cannot act at the same time and CSB should be 100ns wider than /WR (or /RD).

| Product No. | LWD70620EW640G320WID | REV. A | Page | 9 / 23 |
|-------------|----------------------|--------|------|--------|



#### 6.2 SYSTEM BUS TIMING FOR 8080 SERIES MPU



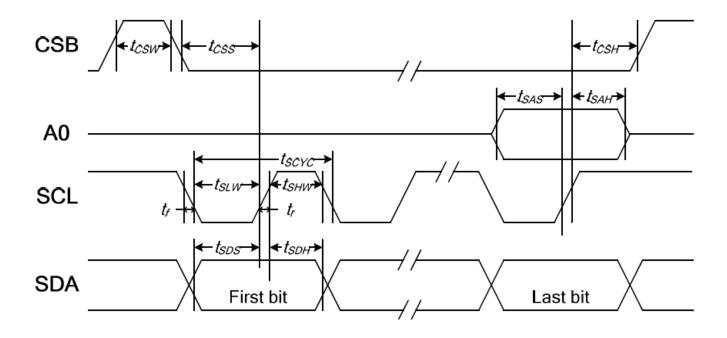
| Item                          | Signal | Symbol | Condition    | Min | Max   | Unit |
|-------------------------------|--------|--------|--------------|-----|-------|------|
| Address setup time            | A0     | tAW8   | -            | 10  | -     |      |
| Address hold time             | AU     | tAH8   | •            | 0   | -     |      |
| System cycle time             |        | tCYC8  | ı            | 200 | -     |      |
| /WR L pulse width (WRITE)     | /WR    | tCCLW  | 1            | 100 | -     |      |
| /WR H pulse width (WRITE)     |        | tCCHW  | 1            | 100 | -     |      |
| /RD L pulse width (READ)      | /RD    | tCCLR  | 1            | 120 | -     |      |
| /RD H pulse width (READ)      | /KD    | tCCHR  | 1            | 120 | 120 - |      |
| CSB setup time                | CSB    | tCSS8  | -            | 100 | -     |      |
| CSB hold time                 | CSB    | tCSH8  | -            | 100 | -     |      |
| Write data setup time         |        | tDS8   | 1            | 70  | -     |      |
| Write data hold time          | D[7.0] | tDH8   | 1            | 20  | -     |      |
| Read data access time         | D[7:0] | tACC8  | CL = 100  pF | -   | 80    |      |
| Read data output disable time |        | tOH8   | CL = 100 pF  | 15  | 80    |      |

- 1. The input signal rise time and fall time (tr, tf) is specified at 15 ns or less. When the system cycle time is extremely fast,(tr + tf)  $\leq$  (tCYC8 tCCLW tCCHW) for (tr + tf)  $\leq$  (tCYC8 tCCLR tCCHR) are specified.
- 2. All timing is specified using 20% and 80% of VDDI as the reference.
- 3. tCCLW and tCCLR are specified as the overlap between CSB being "L" and /WR and /RD being at the "L" level.CSB and /WR (or /RD) cannot act at the same time and CSB should be 100ns wider than /WR (or /RD).

| Product No. | LWD70620EW640G320WID | REV. A | Page | 10 / 23 |
|-------------|----------------------|--------|------|---------|



# 6.3 SYSTEM BUS TIMING FOR 4-LINE SERIAL INTERFACE



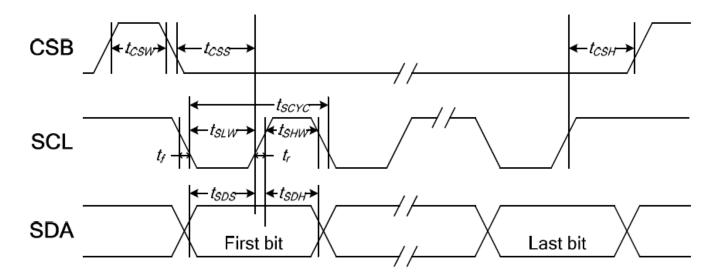
| Item                | Signal | Symbol | Condition | Min | Max | Unit |
|---------------------|--------|--------|-----------|-----|-----|------|
| Serial clock period |        | tSCYC  | -         | 80  | -   |      |
| SCL "H" pulse width | SCL    | tSHW   | -         | 40  | -   |      |
| SCL "L" pulse width |        | tSLW   | -         | 40  | -   |      |
| Address setup time  | A0     | tSAS   | -         | 40  | -   |      |
| Address hold time   | Au     |        | -         | 40  | _   | ***  |
| Data setup time     | SDA    | tSDS   | -         | 15  | -   | ns   |
| Data hold time      | SDA    | tSDH   | -         | 20  | -   |      |
| CSB-SCL time        |        | tCSS   | -         | 40  | -   |      |
| CSB-SCL time        | CSB    | tCSH   | -         | 40  | -   |      |
| CSB "H" pulse width |        | tCSW   | -         | 15  | _   |      |

- 1. The input signal rise and fall time (tr, tf) are specified at 15 ns or less.
- 2. All timing is specified using 20% and 80% of VDDI as the standard.

| Product No. LWD70620EW640G320WID | REV. A |  | Page | 11 / 23 |  |
|----------------------------------|--------|--|------|---------|--|
|----------------------------------|--------|--|------|---------|--|



# 6.4 SYSTEM BUS TIMING FOR 3-LINE SERIAL INTERFACE



| Item                | Signal | Symbol | Condition | Min | Max | Unit |
|---------------------|--------|--------|-----------|-----|-----|------|
| Serial clock period |        | tSCYC  | 1         | 80  | -   |      |
| SCL "H" pulse width | SCL    | tSHW   | 1         | 40  | -   |      |
| SCL "L" pulse width |        | tSLW   | -         | 40  | -   |      |
| Data setup time     | SDA    | tSDS   | -         | 15  | -   | na   |
| Data hold time      | SDA    | tSDH   | 1         | 20  | -   | ns   |
| CSB-SCL time        |        | tCSS   | -         | 40  | -   |      |
| CSB-SCL time        | CSB    | tCSH   | -         | 40  | -   |      |
| CSB "H" pulse width |        | tCSW   | -         | 15  | -   |      |

- 1. The input signal rise and fall time (tr, tf) are specified at 15 ns or less.
- 2. All timing is specified using 20% and 80% of VDDI as the standard.

| Product No. LWD70620EW640G320WID REV | . A |
|--------------------------------------|-----|
|--------------------------------------|-----|

| Page | 12 / 23 |
|------|---------|

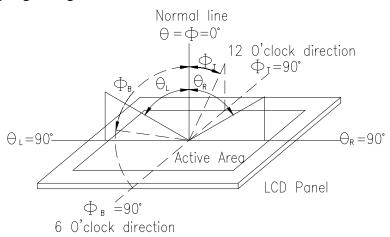


#### 7 OPTICAL CHARACTERISTICS

| Item           |      | Symbol | Temp         | Condition.                 | Min   | Min Typ. Max. |   | Unit              | Remark                  |
|----------------|------|--------|--------------|----------------------------|-------|---------------|---|-------------------|-------------------------|
| Dagmanas tima  |      | Tr     | 25℃          | 0-00 4-0                   | -     | 8             | - |                   | Note 2                  |
| Response time  |      | Tf     | 25℃          | θ=0°, Φ=0                  | _     | 12            | - | .ms               | Note 3                  |
| Contrast ratio |      | CR     | 25℃          | At optimized viewing angle | - 800 |               | - | -                 | Note 4                  |
| Viewing angle  | Hor. | ΘR     | 25℃          |                            |       | 80            |   |                   |                         |
| (Gray Scale    | nor. | ΘL     | 25°C CD □ 10 |                            |       | 80            |   | Dog               | Note 1                  |
| Inversion      | Ver. | ΦВ     | 25℃          | CR□10                      | 80    |               |   | Deg.              | Note 2                  |
| Direction)     | ver. | ΦТ     | 25℃          |                            | 80    |               |   |                   |                         |
| Brightness     |      | -      | 25℃          | -                          | 500   | 600           | - | cd/m <sup>2</sup> | Center<br>of<br>display |

Ta=25±2°C, IL=160mA

#### 1. Definition of viewing angle range



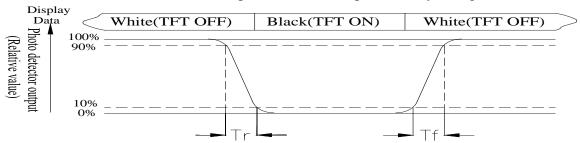
2. Test equipment setup: After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7(BM-5) luminance meter 1.0° field of view at a distance of 50cm and normal direction.

| Product No. LWD70620EW640G320WID | REV. A |  | Page | 13 / 23 |
|----------------------------------|--------|--|------|---------|
|----------------------------------|--------|--|------|---------|



#### 3. Definition of Response time:

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time, Tr, is the time between photo detector output intensity changed from 90% to 10%. And fall time, Tf, is the time between photo detector output intensity changed from 10% to 90%



#### 4. Definition of contrast ratio:

The contrast ratio is defined as the following expression.

Contrast ratio (CR) = Luminance measured when LCD on the "White" state

Luminance measured when LCD on the "Black" state

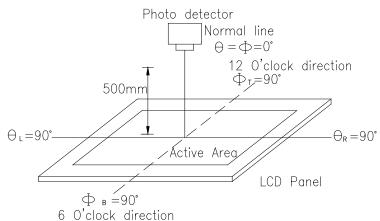
#### 5. White $Vi = Vi50 \pm 1.5V$ Black $Vi = Vi50 \pm 2.0V$

"±" means that the analog input signal swings in phase with VCOM signal.

"±" means that the analog input signal swings out of phase with VCOM signal.

The 100% transmission is defined as the transmission of LCD panel when all the input terminals of module are electrically opened.

6. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.



| Product No. LWD70620EW640G320WID REV. | . A Page | 14 / 23 |
|---------------------------------------|----------|---------|
|---------------------------------------|----------|---------|



# **8 INTERFACE PIN ASSIGNMENT**

| Pin | Symbol       | Function            | Remark |
|-----|--------------|---------------------|--------|
| 1   | GND          | System ground       |        |
| 2   | VDD          | Power Supply: +3.3V |        |
| 3   | NC           | No connect          |        |
| 4   | A0           | Data/Command select |        |
| 5   | /WR(R/W)     | Write strobe signal |        |
| 6   | /RD(E)       | Read strobe signal  |        |
| 7   | DB0          | Data bus            |        |
| 8   | DB1          | Data bus            |        |
| 9   | DB2          | Data bus            |        |
| 10  | DB3          | Data bus            |        |
| 11  | DB4          | Data bus            |        |
| 12  | DB5          | Data bus            |        |
| 13  | DB6          | Data bus            |        |
| 14  | DB7          | Data bus            |        |
| 15  | /CS          | Chip select         |        |
| 16  | /RESET(RSTB) | Hardware reset      |        |
| 17  | IF0          | Mode select         | Note1  |
| 18  | IF1          | Wode select         | Note1  |
| 19  | NC           | No connect          |        |
| 20  | NC           | No connect          |        |
| 21  | NC           | No connect          |        |
| 22  | NC           | No connect          |        |

#### Note:

1.

| ٠ |         |     |                          |     |                        |     |     |                            |  |  |  |
|---|---------|-----|--------------------------|-----|------------------------|-----|-----|----------------------------|--|--|--|
|   | Setting |     | MCIITema                 |     | Interface Pin Function |     |     |                            |  |  |  |
|   | IF1     | IF0 | MCU Type                 | CSB | <b>A0</b>              | RWR | ERD | D[7:0]                     |  |  |  |
|   | L       | L   | Parallel 8080 series MCU |     |                        | /WR | /RD | D[7:0]                     |  |  |  |
| Ī | L       | Н   | Parallel 6800 series MCU | CSB | A0                     | R/W | Е   | D[7:0]                     |  |  |  |
|   | Н       | Н   | Serial 4-Line series MCU | CSB |                        | -   | ı   | D7=SCL, D0=SDA, D[6:1] are |  |  |  |
|   | Н       | L   | Serial 3-Line series MCU |     | -                      | -   | -   | not used                   |  |  |  |

The un-used pins are marked as "-" and should be connected to "H" by VDDI.

|             |                      |        | _ |      |         |
|-------------|----------------------|--------|---|------|---------|
| Product No. | LWD70620EW640G320WID | REV. A |   | Page | 15 / 23 |



# 8.1 BACKLIGHT UNIT SECTION

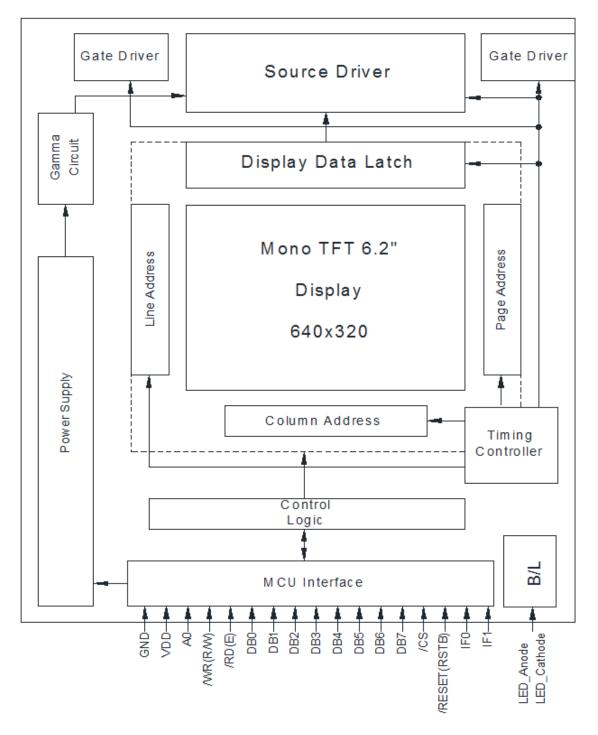
LED Light Bar connector is used for the the integral backlight system. The recommended model is "JST XH-3" manufactured by JST.

| Pin No. | Symbol  | I/O | Function                          | Remark |
|---------|---|-----|-----------------------------------|--------|
| 1       | V <sub>LED+</sub>                             | P   | Power for LED backlight anode (A) | Red    |
| 3       | 3 VLED- P Power for LED backlight cathode (K) |     | Black                             |        |

| Page | 16 / 23 |
|------|---------|

# Densitron

## 9 BLOCK DIAGRAM



|             |                      |        | _ |      |         |
|-------------|----------------------|--------|---|------|---------|
| Product No. | LWD70620EW640G320WID | REV. A |   | Page | 17 / 23 |

#### **10 RELIABILITY**



#### **10.1 RELIABILITY TEST**

Content of Reliability Test (Wide temperature, -30°C ~ 80°C)

| Environmental Test      |  |                      |      |
|-------------------------|--|----------------------|------|
| Test Item               | Content of Test  | Test Condition       | Note |
| High Temperature        | Endurance test applying the high storage                                   | 80°C                 | 2    |
| storage                 | temperature for a long time.   | 200hrs               |      |
| Low Temperature         | Endurance test applying the low storage                                    | -30°C                | 1,2  |
| storage                 | temperature for a long time.   | 200hrs               |      |
| High Temperature        | Endurance test applying the electric stress                                | 70°C                 |      |
| Operation               | (Voltage & Current) and the thermal stress to the element for a long time. | 200hrs               |      |
| Low Temperature         | Endurance test applying the electric stress under                          | -20°C                | 1    |
| Operation               | low temperature for a long time.   | 200hrs               |      |
| High Temperature/       | The module should be allowed to stand at 60                                | 60°C,90%RH           | 1,2  |
| Humidity Operation      | °C,90%RH max   | 96hrs                |      |
| Thermal shock           | The sample should be allowed stand the                                     | -20°C/70°C           |      |
| resistance              | following 10 cycles of operation   | 10 cycles            |      |
|                         | -20°C 25°C 70°C  |                      |      |
|                         |  |                      |      |
|                         | 30min 5min 30min   |                      |      |
|                         | 1 cycle  |                      |      |
| Vibration test          | Endurance test applying the vibration during                               | Total fixed          | 3    |
|                         | transportation and using.  | amplitude: 15mm      |      |
|                         |  | Vibration Frequency: |      |
|                         |  | 10~55Hz              |      |
|                         |  | One cycle 60 seconds |      |
|                         |  | to 3 directions of   |      |
|                         |  | X,Y,Z for Each 15    |      |
|                         |  | minutes              |      |
| Static electricity test | Endurance test applying the electric stress to the                         | VS=±600V(contact),   |      |
|                         | terminal.  | ±800v(air),          |      |
|                         |  | RS=330 Ω             |      |
|                         |  | CS=150pF             |      |
|                         |  | 10 times             |      |

- 1. No dew condensation to be observed.
- 2. The function test shall be conducted after 4 hours of s torage at the normal temperature and humidity after removing from the test chamber
- 3. The packing must include the vibration test.

|             |                      |        | - |      |         |   |
|-------------|----------------------|--------|---|------|---------|---|
| Product No. | LWD70620EW640G320WID | REV. A |   | Page | 18 / 23 | ı |



## 11 INITIAL CODE

```
void Initial_code()
      Write_Command(0xae);
    Write_Data(0xa5);
  Write_Command(0x61);
  Write Data(0x0f);
  Write_Data(0x04);
  Write Data(0xa5);
  Write_Data(0xa5);
    Write_Command(0x62);
    Write_Data(0x00);
    Write_Data(0x3b);
    Write_Data(0x1b);
    Write_Data(0xa5);
    Write_Command(0x63);
    Write Data(0x05);
    Write_Data(0x0f);
    Write Data(0xa5);
    Write_Data(0xa5);
      Write_Command(0x24);
  Write Data(0x01);
    Write_Data(0xa5);
    Write_Data(0xa5);
    Write_Data(0xa5);
      Write_Command(0x22);
      Write_Data(0x02);
      Write_Data(0xa5);
      Write_Data(0xa5);
      Write_Data(0xa5);
      Write_Command(0x91);
      Write_Data(0x00);
      Write_Data(0x21);
      Write_Data(0x23);
      Write_Data(0x24);
      Write_Command(0x92);
      Write_Data(0x27);
      Write_Data(0x28);
      Write_Data(0x29);
```

| Product No.   LWD70620EW640G320WID   REV. A |  |
|---|--|
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Page 19 / 23



```
Write_Data(0x2a);
    Write_Command(0x93);
    Write_Data(0x2b);
    Write_Data(0x2c);
    Write_Data(0x2d);
    Write_Data(0x2e);
    Write Command(0x94);
    Write_Data(0x30);
    Write Data(0x31);
    Write_Data(0x32);
    Write_Data(0x3f);
    Write_Command(0x99);
    Write_Data(0x00);
    Write_Data(0x21);
    Write_Data(0x23);
    Write_Data(0x26);
    Write_Command(0x9a);
    Write_Data(0x27);
    Write_Data(0x28);
    Write_Data(0x29);
    Write_Data(0x2a);
    Write_Command(0x9b);
    Write_Data(0x2b);
    Write Data(0x2c);
    Write_Data(0x2d);
    Write_Data(0x2e);
    Write_Command(0x9c);
    Write_Data(0x30);
    Write_Data(0x35);
    Write_Data(0x3b);
    Write_Data(0x3f);
Write_Command(0x12);
Write_Data(0xa5);
Write_Command(0x15);
Write_Data(0xa5);
```

| Product No. LWD70620EW640G320WID REV. A |
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}



# 12 PART NUMBER LABELLING

All parts will be labelled with the below Part Number.

DENSITRON LWD70620EW640G320WID Rev X TAIWAN YYMM

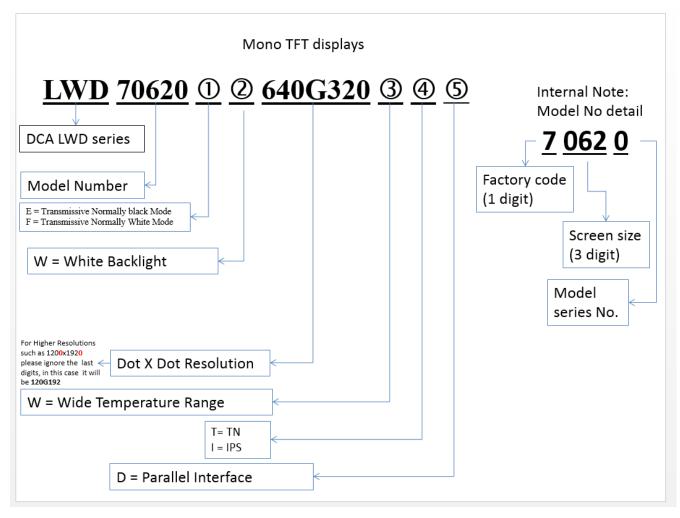
\* where "Rev X" denotes the current Revision and YY and MM the 2-digit Year and Month of manufacture

Product No. LWD70620EW640G320WID REV. A

Page 21 / 23



#### 13 PART NUMBER DECODER



| 8 | Product No. | LWD70620EW640G320WID | REV. A |  | Page | 22 / 23 |
|---|-------------|----------------------|--------|--|------|---------|
|---|-------------|----------------------|--------|--|------|---------|





#### Safety

If the LCD panel breaks, be careful not to get the liquid crystal fluid in your mouth or in your eyes. If the liquid crystal touches your skin or clothes, wash it off immediately using soap and plenty of water.

#### Mounting and Design

Place a transparent plate (e.g. acrylic, polycarbonate or glass) on the display surface to protect the display from external pressure. Leave a small gap between the transparent plate and the display surface. When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding air

When assembling with a zebra connector, clean the surface of the pads with alcohol and keep the surrounding an very clean. Design the system so that no input signal is given unless the power supply voltage is applied.

## Caution during LCD Cleaning

Lightly wipe the display surface with a soft cloth soaked with Isopropyl alcohol, Ethyl alcohol or Trichlorotriflorothane. Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use aromatic solvents (toluene and xylene), or ketonic solvents (ketone and acetone).

#### Caution against Static Charge

As the display uses C-MOS LSI drivers, connect any unused input terminals to VDD or VSS. Do not input any signals before power is turned on. Also, ground your body, work / assembly table and assembly equipment to protect against static electricity.

#### **Packaging**

Displays use LCD elements, and must be treated as such. Avoid strong shock and drop from a height. To prevent displays from degradation, do not operate or store them exposed directly to sunlight or high temperature / humidity.

#### Caution during Operation

It is indispensable to drive the display within the specified voltage limit since excessive voltage shortens its life. Direct current causes an electrochemical reaction with remarkable deterioration of the display quality. Give careful consideration to prevent direct current during ON/OFF timing and during operation.

Response time is extremely delayed at temperatures lower than the operating temperature range while, at high temperatures, displays become dark. However, this phenomenon is reversible and does not mean a malfunction or a display that has been permanently damaged. If the display area is pushed on hard during operation, some graphics will be abnormally displayed but returns to a normal condition after turning off the display once. Even a small amount of condensation on the contact pads (terminals) can cause an electro-chemical reaction which causes missing rows and columns. Give careful attention to avoid condensation.

#### **Storage**

Store the display in a dark place where the temperature is  $25^{\circ}C \pm 10^{\circ}C$  and the humidity below 50% RH. Store the display in a clean environment, free from dust, organic solvents and corrosive gases. Do not crash, shake or jolt the display (including accessories).

|  | Product No. | LWD70620EW640G320WID | REV. A |  | Page | 23 / 23 |
|--|-------------|----------------------|--------|--|------|---------|
|--|-------------|----------------------|--------|--|------|---------|